

## Shekhar Y. Borkar

Intel Fellow, Intel Labs

Director, Microprocessor Research

INTEL CORPORATION

### Patents

- » 6,847,617, Systems for interchip communication/1/25/2005
- » 6,845,424, Memory pass-band signaling/1/18/2005
- » 6,809,538, Active cooling to reduce leakage power/10/26/2004
- » 6,747,474, Integrated circuit stubs in a point-to-point system, 6/8/2004
- » 6,741,107, Synchronous clock generator for integrated circuits, 5/25/2004
- » 6,593,799, Circuit including forward body bias from supply voltage and ground nodes, 7/15/2003
- » 6,538,584, Transition reduction encoder using current and last bit sets, 3/25/2003
- » 6,536,025, Receiver deskewing of multiple source synchronous bits from a parallel bus, 3/18/2003
- » 6,484,265, Software control of transistor body bias in controlling chip parameters/11/19/2002
- » 6,462,943, Method and apparatus for retrofit mounting a VLSI chip to a computer chassis for current supply/10/8/2002
- » 6,441,649, Rail-to-rail input clocked amplifier, 8/27/2002
- » 6,437,601, Using a timing strobe for synchronization and validation in a digital logic device, 8/20/2002
- » 6,415,388, Method and apparatus for power throttling in a microprocessor using a closed loop feedback system, 7/2/2002
- » 6,411,156, Employing transistor body bias in controlling chip parameters, 6/25/2002
- » 6,380,781, Soft error rate tolerant latch, 4/30/2002
- » 6,373,289, Data and strobe repeater having a frequency control unit to re-time the data and reject delay variation in the strobe, 4/16/2002
- » 6,366,156, Forward body bias voltage generation systems, 4/2/2002
- » 6,300,819, Circuit including forward body bias from supply voltage and ground nodes/10/9/2001
- » 6,272,666, Transistor group mismatch detection and reduction, 8/7/2001
- » 6,266,288, Methods for reducing the effects of power supply distribution related noise, 7/24/2001
- » 6,232,827, Transistors providing desired threshold voltage and reduced short channel effects with forward body bias, 5/15/2001
- » 6,218,895, Multiple well transistor circuits having forward body bias, 4/17/2001
- » 6,218,892, Differential circuits employing forward body bias, 4/17/2001

- » 6,137,688, Apparatus for retrofit mounting a VLSI chip to a computer chassis for current supply/10/24/2000
- » 6,111,804, Methods for reducing the effects of power supply distribution related noise, 8/29/2000
- » 6,100,751, Forward body biased field effect transistor providing decoupling capacitance, 8/8/2000
- » 6,072,740, Apparatus for reducing the effects of power supply distribution related noise, 6/6/2000
- » 6,067,232, System for connecting subsystems of dissimilar thermal properties, 5/23/2000
- » 6,018,465, Apparatus for mounting a chip package to a chassis of a computer/1/25/2000
- » 5,978,228, Apparatus for mounting a very large scale integration (VLSI) chip to a computer chassis for cooling/11/2/1999
- » 5,969,944, Method and apparatus for mounting a very large scale integration (VLSI) chip package to a computer chassis for cooling/10/19/1999
- » 5,943,270, Two-transistor DRAM cell for logic process technology, 8/24/1999
- » 5,880,945, Power conversion and load circuit on same integrated circuit, 3/9/1999
- » 5,634,043, Microprocessor point-to-point communication, 5/27/1997
- » 5,623,644, Point-to-point phase-tolerant communication, 4/22/1997
- » 5,604,450, High speed bidirectional signaling scheme/2/18/1997
- » 5,546,023, Daisy chained clock distribution scheme, 8/13/1996
- » 5,063,308, Output driver with static and transient parts/11/5/1991
- » 5,047,922, Virtual I/O, 9/10/1991
- » 4,975,598, Temperature, voltage, and process compensated output driver, December 4/1990
- » 4,782,439, Direct memory access system for microcontroller/11/1/1988

## Publications

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