



# **Intel® Desktop Board D925XCV Specification Update**

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Order Number: C80897-006

The Intel® Desktop Board D925XCV may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

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The Intel® desktop board D925XCV may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

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## REVISION HISTORY

<b>Date of Revision</b>	<b>Version</b>	<b>Description</b>
July 2004	-001	This document is the first Specification Update for the Intel® Desktop Board D925XCV.
September 2004	-002	Added Documentation Change 1.
October 2004	-003	Added Erratum 1. Added Documentation Change 2.
December 2004	-004	Removed Documentation Changes and incorporated them into revision -002 of the Technical Product Specification.
February 2005	-005	Added Erratum 2.
March 2005	-006	Added Erratum 3.

## PREFACE

This document is an update to the specifications contained in the *Intel® Desktop Board D925XCV Technical Product Specification* (Order number C68597). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order number 249199) for specification updates concerning the Intel Pentium 4 processor and that may apply to the desktop board D925XCV. Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 925X Chipset Specification Update* (Order Number 301465) for specification updates concerning the 82925X MCH Controller and that may apply to the desktop board D925XCV. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® IO Controller Hub 6 (ICH6) Family Specification Update* (Order Number 301474) for specification updates concerning the 82801ER I/O Controller Hub and that may apply to the desktop board D925XCV. Unless otherwise noted in this document, it should be assumed that any ICH 6 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

## Nomenclature

**Specification Changes** are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

**Errata** are design defects or errors. Characterized errata may cause the desktop board D925XCV's behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

**Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

**Documentation Changes** include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

**Specification Update for the  
Intel® Desktop Board D925XCV**





## GENERAL INFORMATION

**Basic Desktop Board D925XCV Identification Information**

AA Revision	BIOS Revision	Notes
C57587-401	CV92510A.86A.0193	1-5
C57587-402	CV92510A.86A.0193	1-5
C57587-403	CV92510A.86A.0210	1-5
C57587-404	CV92510A.86A.0249	1-5
C67680-401	CV92510A.86A.0183	1-5
C67680-402	CV92510A.86A.0193	1-5
C67680-403	CV92510A.86A.0210	1-5
C67680-404	CV92510A.86A.0249	1-5

NOTES:

1. The AA number is found on a small label on the component side of the board.
2. The 82925X Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping	S-Spec Numbers
82925X MCH	B1 B2	SL7LZ SL7RC
82801FR ICH6	B1	SL79N

3. Refer to the *Intel® Pentium® 4 Processor Specification Update* (Order Number 249199) for errata related to the Pentium 4 processor and that may apply to the desktop board D925XCV.
4. Refer to the *Intel® 925X Chipset Specification Update* (Order Number 301465) for errata related to the 82925X MCH that may apply to the desktop board D925XCV.
5. Refer to the *Intel® IO Controller Hub 6 (ICH6) Family Specification Update* (Order Number 301474) for errata related to the 82801FB I/O Controller Hub that may apply to the desktop board D925XCV.

## Summary Table of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board D925XCV. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted. This table uses the following notations:

### CODES USED IN SUMMARY TABLE

Doc:	Document change or update that will be implemented.
PlanFix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
NoFix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

NO.	PLANS	ERRATA
1	Plan Fix	Alert Standard Format (ASF) is non-functional
2	Plan Fix	BIOS corruption may occur if power is interrupted during the BIOS flash procedure, resulting in the system's inability to POST
3	Plan Fix	Wake On LAN may not function after loss and restore of AC power
NO.	PLANS	DOCUMENTATION CHANGES
3	Doc	All previous Documentation Changes incorporated

## ERRATA

### 1. ***Alert Standard Format (ASF) is Non-Functional***

**PROBLEM:** BIOS and device driver support is currently not implemented to support Alert Standard Format (ASF).

**IMPLICATION:** ASF, an interface that provides access and manageability in OS-absent environments, is currently not supported.

**WORKAROUND:** None.

**STATUS:** This erratum may be fixed in a future BIOS and driver revision.

### 2. ***BIOS Corruption May Occur if Power is Interrupted During the BIOS Flash Procedure, Resulting in the System's Inability to POST***

**PROBLEM:** Power interruption during BIOS flash process may corrupt certain non-bootblock areas in the Firmware Hub.

**IMPLICATION:** If power fails during the flash process, the new BIOS image should be preserved. In some instances, a power failure during the flash process may instead cause corruption and not save the new image to the Firmware Hub resulting in a no POST condition; a POST card will indicate a halt at POST code D7h.

**WORKAROUND:** If power fails during the BIOS flash process and a no POST condition exists upon subsequent boot, implementing the BIOS flash recovery procedure will return the BIOS to a stable state.

**STATUS:** This erratum may be fixed in a future BIOS revision.

### 3. ***Wake On LAN May Not Function After Loss and Restore of AC Power***

**PROBLEM:** If AC power is removed, Wake On LAN may not function until the LAN circuit is re-initialized. The sequence of events is as follows:

- AC power removed (AC power cord disconnected, for example)
- AC power restored. +5V Standby voltage is re-applied to the board
- Wake On LAN may not be functional
- Switch power on
- Switch power off to put system back into Standby mode (S5 state)
- Wake On LAN functionality restored

**IMPLICATION:** Wake on LAN will not function if AC power is interrupted.

**WORKAROUND:** In the BIOS Setup program, set After Power Failure to "Power On" in the Power Menu.

**STATUS:** This erratum may be fixed in a future BIOS revision.



## DOCUMENTATION CHANGES

The Documentation Changes listed in this section apply to the *Intel® Desktop Board D925XCV Technical Product Specification* (Order Number C68597). All Documentation Changes will be incorporated into a future version of that specification.

### **1. *All previous Documentation Changes incorporated***

The Documentation Changes listed in previous versions of this document have been incorporated into a revision of the *Intel® Desktop Board D925XCV Technical Product Specification* (Order number C68597).