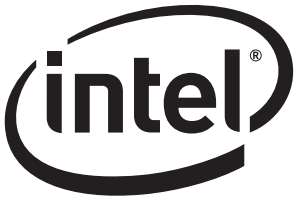

Intel® Desktop Board D975XBX2

Specification Update

July 2008

Order Number: D74278-003US

The Intel® Desktop Board D975XBX2 may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are documented in this Specification Update.

Revision History

Revision	Revision History	Date
-001	This document is the first Specification Update for the Intel® Desktop Board D975XBX2.	October 25, 2006
-002	Update General Information Section.	December 2007
-003	Update General Information and Specification Changes Sections.	July 2008

Contents

Specfication Update for the Intel® Desktop Board D975XBX2

This document is an update to the specifications contained in the *Intel® Desktop Board D975XBX2 Technical Product Specification* (Order number D73645). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

For specification updates concerning the Intel processor that may apply to this desktop board, refer to the following:

- Intel® Core™2 Extreme Processor x6800Δ and Intel® Core™2 Duo Desktop Processor E6600Δ Sequence Specification Update (Order Number 313279)
- Intel® Pentium® D Processor 900Δ Sequence and Intel® Pentium® Processor Extreme Edition 955Δ, 965Δ Specification Update (Order Number 310307)
- Intel® Pentium® D Processor 800Δ Sequence and Intel® Pentium® Processor Extreme Edition 840Δ Specification Update (Order Number 306832)
- Intel® Pentium® 4 Processor 6x1Δ Sequence Specification Update (Order Number 310309)
- Intel® Pentium® 4 Processor on 90 nm Process Specification Update (Order Number 302352)

Unless otherwise noted in this document, it should be assumed that any processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 975X Express Chipset Specification Update* (Order Number 310159) for specification updates concerning the 82975X MCH Controller and that may apply to the desktop board D975XBX2. Unless otherwise noted in this document, it should be assumed that any MCH errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® IO Controller Hub 7 (ICH7) Family Specification Update* (Order number 307014) for specification updates concerning the 82801GDH I/O Controller Hub and that may apply to the desktop board D975XBX2. Unless otherwise noted in this document, it should be assumed that any ICH7 errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Terminology

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

General Information

Basic Desktop Board D975XBX2 Identification Information

AA Revision	BIOS Revision	Notes
D53350-502	BX97520J.86A.2212	1,2
D53350-503	BX97520J.86A.2214	1,2
D53350-504	BX97520J.86A.2330	1,2
D53350-505	BX97520J.86A.2333	1,2
D53350-506	BX97520J.86A.2507	1,2
D53350-507	BX97520J.86A.2674	1,2
D53350-508	BX97520J.86A.2777	1,2
D53350-509	BX97520J.86A.2797	1,2
D53350-510	BX97520J.86A.2813	1,2

NOTES:

1. The AA number is found on a small label on the component side of the board.
2. The 975X Chipset kit used on this AA revision consists of two components as follows:

Device	Stepping
82975X MCH	A0
82801GDH	A1

Summary of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board D975XBX2. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted.

The following notations are used in the table:

Doc:	Document change or update that will be implemented.
Plan Fix:	This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.
Fixed:	This erratum has been previously fixed.
No Fix:	There are no plans to fix this erratum.
Shaded:	This erratum is either new or modified from the previous version of the document.

No.	Plans	Specification Changes
1	Doc	Change to Section 1.4.1.1 to add Channel B in Figures 4 and 5.
No.	**Plans**	**Errata**
		There are no characterized erratum for this product.

Specification Changes

1. Figures 4 and 5 of Section 1.4.1.1 of the Technical Specification will be updated to include both Channels A and B in the Dual Channel (Interleaved) Mode Configurations.

1.4.1.1 Dual Channel (Interleaved) Mode Configurations

Figure 4 shows a dual channel configuration using two DIMMs. In this example, the DIMM0 (blue) sockets of both channels are populated with identical DIMMs.

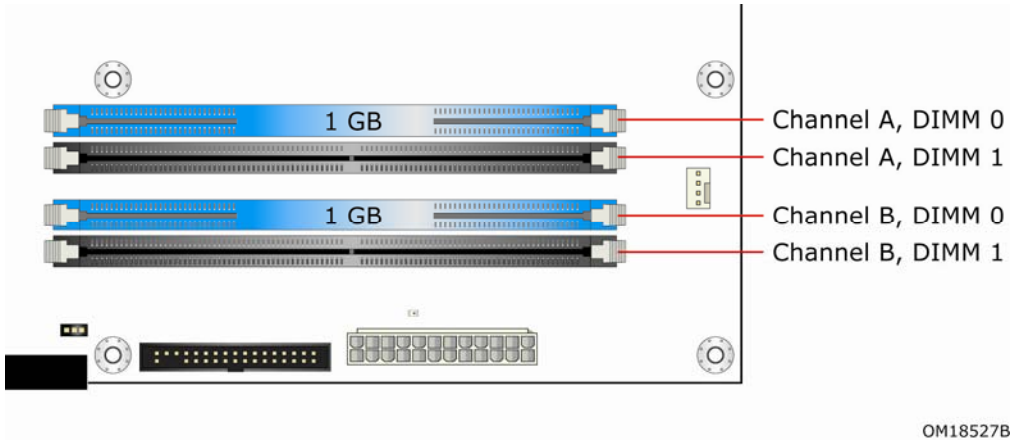

Figure 4. Dual Channel (Interleaved) Mode Configuration with Two DIMMs

Figure 5 shows a dual channel configuration using three DIMMs. In this example, the combined capacity of the two DIMMs in Channel A equal the capacity of the single DIMM in the DIMM0 (blue) socket of Channel B.

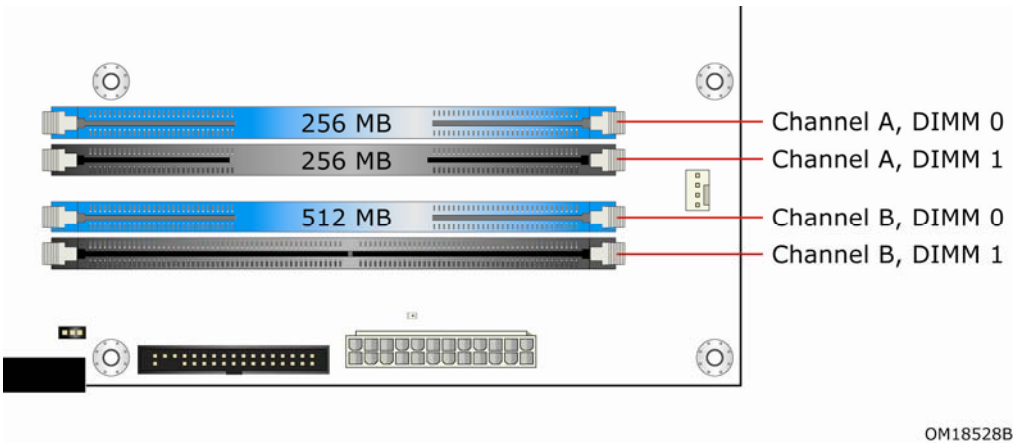

Figure 5. Dual Channel (Interleaved) Mode Configuration with Three DIMMs