SE7500WV2 Server Board
SR2300 Server Chassis
SR1300 Server Chassis

Specification Update

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Revision History

Date	Modifications
August 2002	Initial release.
September 2002	Added –403 ATA and SCSI baseboards to the Product Scope section. Updated Errata 7 "SE7500WV2 based systems will hang and not complete a Rebuild using the onboard Promise* controller with an ATA RAID 1 (Mirror)". Added Errata 8 "Some PCI-X Bus Mastering U320 SCSI Adapters may cause bus contention on SE7500WV2 based systems when installed in the PCI-X Bus B". Added Documentation Change 5 "The SE7500WV2 Technical Product Specification Revision 1.0 incorrectly states that the maximum PCI-X bus speed on the B bus is 133MHz in the SR1300 chassis".
October / November 2002	Changed the BIOS version and availability date for the fix to Errata #7 & #8. Added document change #6 (The SE7500WV2 Technical Product Specification Revision 1.0 incorrectly lists the "Boot Menu Selections" and "Exit Menu Selections" twice in the document), #7 (The SE7500WV2 Technical Product Specification Revision 1.0 incorrectly shows a "Fault Resilient Booting Sub-menu Selections" menu that is not contained in the BIOS Setup), #8 (The SR2300 Chassis Subassembly Product Guide (Order Number A86100-002) incorrectly labels Figure 4 "Controls and Indicators" on Page 11), #9 (The SR2300 Chassis Technical Product Specification Revision 1.0 and the SR1300 Chassis Technical Product Specification Revision 1.0 incorrectly list the Power Supply input voltage ranges), and #10 (The SR1300 Quick Start User Guide does not specify that the foam heatsink gasket that is shipped with the 1U Boxed Intel® Xeon™ processors with 512K cache should not be used with the SR1300 chassis).
December 2002	Changed the BIOS availability date for the fix to Errata #7 & #8 to December 2002.
January 2003	Changed the status for Errata #7 & #8, added the A81418-405 (ATA) & A81417-405 (SCSI) to the "Product Scope" section.

Disclaimers

The SE7500WV2 Server Board, SR2300 Server Chassis, and SR1300 Server Chassis may contain design defects or errors known as errata that may cause the product to deviate from the published specifications. Current characterized errata are documented in this Specification Update.

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Contents

Preface

This document is an update to the product definition specified in the SE7500WV2 Server Board, SR2300 Server Chassis, and SR1300 Server Chassis *Technical Product Specification*s (Order Number A96327-002, A94546-002, and A94544-002). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain specification changes, specification clarifications, errata, and document changes.

Refer to the *Intel® Xeon™ Processor Specification Update* (Document Number 249678-022) for specification updates concerning the Xeon™ processor. Items contained in the *Xeon™ Processor Specification Update* that either do not apply to the SE7500WV2 Server board or have been worked around are noted in this document. Otherwise, it should be assumed that any processor errata for a given stepping are applicable to the Printed Board Assembly (PBA) revisions(s) associated with that stepping.

Nomenclature

- **Specification Changes** are modifications to the current published specifications for the SE7500WV2 Server Board, SR2300 Server Chassis, or SR1300 Server Chassis. These changes will be incorporated in a future release of the given document.

- **Specification Clarifications** describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in a future release of the given document.

- **Documentation Changes** include typos, errors, or omissions from documents that are currently published. These documents may include Product Specs and Users Guides. These changes will be incorporated in a future release of the given document.

- **Errata** are design defects or errors. Errata may cause the SE7500WV2 Server board's, SR2300 Server Chassis', or SR1300 Server Chassis' behavior to deviate from published specifications. Hardware and software designed to be used with any given processor stepping must assume that all errata documented for that processor stepping are present on all devices.

Product Scope

Below are the specific boards, BIOS and components covered by this.

Baseboard Fab #	Baseboard PBA #	BIOS Revision / Build #	BMC Revision
FAB 4	A81418-402 (ATA) A81417-402 (SCSI)	1.0	13
FAB 4	A81418-403 (ATA) A81417-403 (SCSI)	2.0	15
FAB 4	A81418-404 (ATA) A81417-404 (SCSI)	3.0	19
FAB 4	A81418-405 (ATA) A81417-405 (SCSI)	3.0	19

Summary Tables of Changes

The following tables indicate the errata and the document changes that apply to the SE7500WV2 Server Board, SR2300 Server Chassis, and SR1300 Server Chassis. Intel intends to fix some of the specified errata in future updates to the server board and chassis. Documentation changes will be made in future updates to the given document. The tables use the following notations:

Doc: Intel intends to update the appropriate document in a future revision.

Investigating Intel is investigating the issue.

Fix: Intel intends to fix this erratum in a future update of the board or chassis.

Fixed: This erratum has been addressed.

NoFix: There are no plans to fix this erratum.

Shaded: This erratum is either new or has been modified from the previous specification update.

Table 1. Errata Summary

No.	Plans	Description of Errata
1.	Fixed	Intel RAID cards hang SE7500WV2 based systems during POST
2.	Investigating	Single drives must be configured as Master on the Promise* ATA RAID channels
3.	NoFix	Disabling the onboard NICs will produce a POST Error message "PXE-E01"
4.	NoFix	When more than 7 drives are added to the onboard SCSI Channel A, SCSI Channel B drives no longer show up in system BIOS as available
5.	NoFix	Boot Order is reset when the SCSI boot drive is removed
6.	Fixed	RedHat* Linux* 7.3 will not boot with a USB device connected to the SE7500WV2, the system locks and the screen displays "L"
7.	Fixed	SE7500WV2 based systems will hang and not complete a Rebuild using the onboard Promise* controller with an ATA RAID 1 (Mirror)
8.	Fixed	Some PCI-X Bus Mastering U320 SCSI Adapters may cause bus contention on SE7500WV2 based systems when installed in the PCI-X Bus B

Table 2. Documentation Changes

No.	Plans	Description of Documentation Change
1.	Fix	The SR2300 Chassis Quick Start User Guide incorrectly states that there are three shouldered standoffs for server board alignment
2.	Fix	The SR2300 Chassis Quick Start User Guide incorrectly states that four system fans are required for two processor configurations
3.	Fix	The SR2300 2U Server Chassis Technical Product Specification Revision 1.0 incorrectly shows the NIC controllers on the PCI Segment A and the SCSI controller on Segment B
4.	Fix	The SR2300 2U Server Chassis Technical Product Specification Revision 1.0 does not clearly state how an add-in tape drive should be connected
5.	Fix	The SE7500WV2 Technical Product Specification Revision 1.0 incorrectly states that the maximum PCI-X bus speed on the B bus is 133MHz in the SR1300 chassis
6.	Fix	The SE7500WV2 Technical Product Specification Revision 1.0 incorrectly lists the "Boot Menu Selections" and "Exit Menu Selections" twice in the document
7.	Fix	The SE7500WV2 Technical Product Specification Revision 1.0 incorrectly shows a "Fault Resilient Booting Sub-menu Selections" menu that is not contained in the BIOS Setup
8.	Fix	The SR2300 Chassis Subassembly Product Guide (Order Number A86100-002) incorrectly labels Figure 4 "Controls and Indicators" on Page 11
9.	Fix	The SR2300 Chassis Technical Product Specification Revision 1.0 and the SR1300 Chassis Technical Product Specification Revision 1.0 incorrectly list the Power Supply input voltage ranges
10.	Fix	The SR1300 Quick Start User Guide does not specify that the foam heatsink gasket that is shipped with the 1U Boxed Intel® Xeon™ processors with 512K cache should not be used with the SR1300 chassis

Following are in-depth descriptions of each erratum / documentation change indicated in the tables above. The errata and documentation change numbers below correspond to the numbers in the tables.

Errata

1. Intel RAID cards hang SE7500WV2 based systems during POST

Problem   During system boot with an SE7500WV2 based system with an Intel RAID card installed (SRCMR, SRCU32, or SRCU31L), the system will hang and give a POST code "CC". The SE7500WV2 BIOS expects the system to be in "Big Real Mode" after executing the Option ROMS that are installed, so that it can use a 32-bit pointer. If the system is not in "Big Real Mode" when the BIOS uses a 32-bit pointer, it will cause a "General Protection Fault" and the system will hang. These cards can not detect the mode that the system was in before they put it in "Big Real Mode". So after their Option ROMs are expanded, they put the system in "Real Mode".

Implication   Intel RAID cards can not be used with SE7500WV2 based systems.

Workaround   None.

Status   Fixed. This issue is fixed in the SE7500WV2 BIOS Production 2, which is currently available on the Intel website. The SE7500WV2 BIOS now puts the system in "Big Real Mode" after the Option ROMs are expanded.

2. Single drives must be configured as Master on the Promise* ATA RAID channels

Problem   With the SE7500WV2 ATA Baseboard/SR1300 ATA Chassis configuration, if a single IDE ATA drive is installed on the Promise ATA RAID channel, it must be set for Master for proper functionality. The current Promise Option ROM allows the user to configure drives in an array when they are the only drive on the channel and are set for Slave, this gives the appearance that the drive is working fine. The Promise Operting System driver however will not talk to a disk set as Slave, when a Master is not present on that channel.

Implication   With the SE7500WV2 ATA Baseboard/SR1300 ATA Chassis configuration, if a single IDE ATA drive is installed on the Promise ATA RAID channel, it must be set for Master for proper functionality.

Workaround   If a single disk drive is configured on the Promise ATA RAID channel, always set the disk drive for Master. This will allow the Promise Operating System driver to talk to the disk drive.

Status   Investigating.

3. Disabling the onboard NICs will produce a POST Error message "PXE-E01"

Problem   Disabling the two onboard 10/100/1000 NICs in BIOS Setup will produce a "PXE-E01: PCI vendor and device ID's do not match" POST error message. This error is seen immediately after the onboard ATI Rage Video Controller POST's status to the screen. The system will still be functional.

Implication   Disabling the two onboard 10/100/1000 NICs in BIOS Setup will produce a "PXE-E01: PCI vendor and device ID's do not match" POST error message, but the system will still be functional.

Workaround   None.

Status   No Fix.

4. When more than 7 drives are added to the onboard SCSI Channel A, SCSI Channel B drives no longer show up in system BIOS as available

Problem   When more than 7 SCSI drives are added to the onboard SCSI Channel A (external SCSI connector), the onboard SCSI Channel B (internal SCSI connector) drives will not show up in the system BIOS as available. The user will no longer be able to boot from the Drive 0 on SCSI Channel B. This is expected behavior as Adaptec* only uses 3 bits for boot drive addresses, so the SCSI Option ROM is limited to a total of 8 drives.

Implication   When more than 7 SCSI drives are added to the onboard SCSI Channel A (external SCSI connector), the user must modify the Adaptec Option ROM Setup to boot from the Drive 0 on SCSI Channel B.

Workaround   Any of the attached SCSI drives on either Channel A or B can be booted from. The user must modify the Adaptec Option ROM Setup. The default SCSI Channel to boot from can be modified in the "Boot Device Options" menu in the Adaptec Option ROM Setup. In addition, the specific drive to boot from can be specified in the "Boot ID" menu in the Adaptec Option ROM Setup. Each individual drive can also be disabled from being capable of booting in the "SCSI Device Configuration" menu in the Adaptec Option ROM Setup.

Status   No Fix.

5.   Boot Order is reset when the SCSI boot drive is removed

Problem   When the Boot Order in BIOS Setup is set to boot from the onboard SCSI first, the drive will boot fine. If the SCSI boot drive is removed, the BIOS Boot Order will reset, rather than booting to the next boot device in the boot order. This is expected behavior with the BIOS.

Implication   If the SCSI boot drive is removed, the BIOS Boot Order will need to be reset in the BIOS Setup.

Workaround   None.

Status   No Fix.

6.   RedHat* Linux* 7.3 will not boot with a USB device connected to the SE7500WV2, the system locks and the screen displays "L"

Problem   RedHat Linux 7.3 will fail to boot using LILO version 21.4-4 with a USB device connected to the SE7500WV2, the system locks and the screen displays "L". With a USB device connected, LILO version 21.4-4 is setting up it's stack area in the Extended BIOS Data Area (EBDA). This is the same area that the onboard Adaptec* Option ROM uses for it's data area. When LILO version 21.4-4 makes an INT 13h call, the two conflict and the system locks up.

Implication   RedHat Linux 7.3 will not boot on an SE7500WV2 based system using LILO version 21.4-4 with a USB device connected.

Workaround   Boot RedHat Linux 7.3 on SE7500WV2 based systems without USB devices connected, or use GRUB rather than LILO, or use a boot recovery diskette for the first system reboot and upgrade to LILO version 21.4-5 or greater.

Status   Fixed. This issue has been fixed in LILO versions 21.4-5 and greater.

7. SE7500WV2 based systems will hang and not complete a Rebuild using the onboard Promise* controller with an ATA RAID 1 (Mirror)

Problem   SE7500WV2 based systems will hang and not complete a Rebuild using the onboard Promise controller with an ATA RAID 1 (Mirror). If a RAID 1 Mirror is created using the onboard Promise controller with two ATA hard drives, and one of the hard drives fail, the Promise (CRTL-F) Rebuild process will not complete. The Rebuild progress window will come up showing the "%" symbol, but the Rebuild will not progress and the system will lock-up.

Implication   SE7500WV2 based systems can not Rebuild a failed drive in an ATA RAID 1 (Mirror) configuration with the onboard Promise controller.

Workaround   If the onboard 10/100/1000 NIC's are disabled on the SE7500WV2 baseboard through BIOS Setup, the ATA RAID 1 (Mirror) Rebuild process will complete successfully.

Status   Fixed. This issue is fixed in the SE7500WV2 BIOS Production 5 (Release 4.00), which is posted on the Intel website.

8. Some PCI-X Bus Mastering U320 SCSI Adapters may cause bus contention on SE7500WV2 based systems when installed in the PCI-X Bus B

Problem   Some PCI-X Bus Mastering U320 SCSI Adapters may cause bus contention on SE7500WV2 based systems when installed in the PCI-X Bus B. If the affected PCI-X SCSI Adapter requests and is granted the bus but then removes the request without running a cycle, it is possible for the PCI-X arbiter to issue back-to-back grants to other cards without the required 1 clock delay for turn around time, resulting in bus contention. See the Intel® P64H2 Specification Update for more details on this issue.

Implication   An SE7500WV2 based system with a PCI-X Bus Mastering U320 SCSI Adapter installed in PCI-X Bus B that exhibits the behavior described, may encounter system hangs or blue screens. Data integrity is protected by PCI parity protocol. The SE7500WV2 PCI-X Bus C is not affected by this issue, as it is already operating in PCI 66 mode due to the Adaptec* 7899W* SCSI part that is on the bus.

Workaround   A BIOS workaround will be implemented in the SE7500WV2 BIOS to detect the PCI-X device(s) that would exhibit this behavior on PCI Bus B. Once detected, the BIOS will initialize the bus segment to operate in PCI 66 mode. Refer to the Intel® P64H2 BIOS Specification Update Rev. 1.3 for more details of the BIOS workaround.

Status   Fixed. This issue is fixed in the SE7500WV2 BIOS Production 5 (Release 4.00), which is posted on the Intel website.

Documentation Changes

1. The SR2300 Chassis Quick Start User Guide incorrectly states that there are three shouldered standoffs for server board alignment

Problem   The Note in Step #4-A of the Intel Server Chassis SR2300 Quick Start User Guide incorrectly states that there are three shouldered standoffs for server board alignment in the Intel Server Chassis SR2300. There are actually only two shouldered standoffs for server board alignment in the Intel Server Chassis SR2300. The middle standoff is not shouldered.

Implication   There are only two shouldered standoffs in the Intel Server Chassis SR2300 for server board alignment.

Status   The next revision of the Intel Server Chassis SR2300 Quick Start User Guide will be corrected to say that there are only two shouldered standoffs in the Intel Server Chassis SR2300 for server board alignment.

2. The SR2300 Chassis Quick Start User Guide incorrectly states that four system fans are required for two processor configurations

Problem   Step #7-B of the Intel Server Chassis SR2300 Quick Start User Guide incorrectly states that step #7-B must be completed for two processor system configurations. Step #7-B is the step to add a forth system fan. The forth system fan is not required for two processor system configurations, it is to provide redundant system cooling. The three system fan configuration will cool two processor configurations sufficiently in the Intel Server Chassis SR2300.

Implication   The four system fan configuration of the Intel Server Chassis SR2300 is only required for redundant cooling, the three system fan configuration will cool the two processor configuration sufficiently.

Status   The next revision of the Intel Server Chassis SR2300 Quick Start User Guide will be corrected to say that step #7-B is only required for redundant system cooling.

3.   The SR2300 2U Server Chassis Technical Product Specification Revision 1.0 incorrectly shows the NIC controllers on the PCI Segment A and the SCSI controller on Segment B

Problem   The SR2300 2U Server Chassis Technical Product Specification Revision 1.0 incorrectly shows the NIC controller on PCI Segment A and the SCSI controller on Segment B. Page 55, section 9 "Supported Intel® Server Boards", incorrectly shows the two network interface controllers: Intel® 82546PM Fast Ethernet Controllers on PCI Segment A. The two network interface controllers: Intel® 82546PM Fast Ethernet Controllers are actually on PCI Segment B. Page 55, section 9 "Supported Intel® Server Boards", also incorrectly shows the Dual-channel wide Ultra-160 SCSI controller providing one internal and one high density external channel support: Adaptec* AIC-7899W SCSI controller (SCSI SE7500WV2 server board only) on PCI Segment B. The Dual-channel wide Ultra-160 SCSI controller providing one internal and one high density external channel support: Adaptec* AIC-7899W SCSI controller (SCSI SE7500WV2 server board only) is actually on PCI Segment C.

Implication   The SE7500WV2 server board has the onboard NIC controllers on PCI Segment B and the onboard SCSI controller on PCI Segment C.

Status   The next revision of the SR2300 2U Server Chassis Technical Product Specification will be corrected to say that the SE7500WV2 server board has the onboard NIC controllers on PCI Segment B and the onboard SCSI controller on PCI Segment C.

4.   The SR2300 2U Server Chassis Technical Product Specification Revision 1.0 does not clearly state how an add in tape drive should be connected

Problem   The SR2300 2U Server Chassis Technical Product Specification Revision 1.0 does not clearly state how an add-in tape drive should be connected. Page 26, section 4.3 Tape Drive Bay states that the SCSI cable from the tape drive should be connected to either the SE7500WV2 onboard SCSI or SCSI PCI add in card. The suggested way of connecting the SCSI cable from the tape drive is to a SCSI PCI add in card. The SE7500WV2 onboard internal SCSI connector will be required to connect to the SR2300 2U Server Chassis SCSI Backplane.

Implication   Integrators who install an add in tape drive will need to purchase a SCSI PCI add in card to connect the add in tape drive to, the SE7500WV2 onboard internal SCSI connector will be required to connect to the SR2300 2U Server Chassis SCSI Backplane.

Status   The next revision of the SR2300 2U Server Chassis Technical Product Specification will be corrected to say that the suggested way of connecting the SCSI cable from the tape drive is to a SCSI PCI add in card. The SE7500WV2 onboard internal SCSI connector will be required to connect to the SR2300 2U Server Chassis SCSI Backplane.

5.   The SE7500WV2 Technical Product Specification Revision 1.0 incorrectly states that the maximum PCI-X bus speed on the B bus is 133MHz in the SR1300 chassis

Problem   The SE7500WV2 Technical Product Specification Revision 1.0 incorrectly states that the maximum PCI-X bus speed on the B bus is 133MHz in the SR1300 chassis. Page 24, Table 3 P64-B Speeds states that the maximum PCI-X bus speed is 133MHz for the "0 Adapter cards installed and on board device enabled" line in the SR1300 chassis (Bus B with Anvik dual NIC down and 1 Slot Riser). The maximum bus speed in this configuration should be 100MHz. The entry in the table should be "PCI-X 64/100" rather than "PCI-X 64/133".

Implication   The maximum bus speed is 100MHz for the "0 Adapter cards installed and on board device enabled" line in the SR1300 chassis (Bus B with Anvik dual NIC down and 1 Slot Riser).

Status   The next revision of the SE7500WV2 Technical Product Specification will be corrected to say that the maximum bus speed is 100MHz for the "0 Adapter cards installed and on board device enabled" line in the SR1300 chassis (Bus B with Anvik dual NIC down and 1 Slot Riser) configuration.   Table 3 P64-B Speeds on page 24 will be updated to reflect this.

6.   The SE7500WV2 Technical Product Specification Revision 1.0 incorrectly lists the "Boot Menu Selections" and "Exit Menu Selections" twice in the document

Problem   The SE7500WV2 Technical Product Specification Revision 1.0 incorrectly lists the "Boot Menu Selections", "Boot Device Priority Selections", "Hard Drive Selections", "Removable Device Selections", and "Exit Menu Selections" twice in the document. Tables 44, 45, 46, 47, and 48 on pages 67 and 68 are duplicated in Tables 50, 51, 52, 53, and 54 on pages 69 and 70. The two table sets are slightly different and Tables 50, 51, 52, 53, and 54 on pages 69 and 70 are correct and should be used for reference.

Implication   The actual SE7500WV2 Server Board <F2> BIOS Seup screens are slightly different from Tables 44, 45, 46, 47, and 48 on pages 67 and 68. So users should use Tables 50, 51, 52, 53, and 54 on pages 69 and 70 for reference.

Status   The next revision of the SE7500WV2 Technical Product Specification will be corrected and have Tables 44, 45, 46, 47, and 48 on pages 67 and 68 removed.

7. The SE7500WV2 Technical Product Specification Revision 1.0 incorrectly shows a "Fault Resilient Booting Sub-menu Selections" menu that is not contained in the BIOS Setup

Problem   The SE7500WV2 Technical Product Specification Revision 1.0 incorrectly shows a table labeled "Table 49. Fault Resilient Booting Sub-menu Selections" on page 68. This Sub-menu containing these options are not present in the SE7500WV2 Server Board <F2> BIOS Setup.

Implication   The "Fault Resilient Booting Sub-menu Selections" are not available to users in the SE7500WV2 Server Board <F2> BIOS Setup.

Status   The next revision of the SE7500WV2 Technical Product Specification will have Table 49 on Page 68 removed.

8. The SR2300 Chassis Subassembly Product Guide (Order Number A86100-002) incorrectly labels Figure 4 "Controls and Indicators" on Page 11

Problem   The SR2300 Chassis Subassembly Product Guide (Order Number A86100-002) incorrectly labels Figure 4 on Page 11. The correct descriptions for the figure are listed below:

A. NIC 2 activity LED

B. NIC 1 activity LED

C. Power button

D. Power/sleep LED

E. Fixed disk drive status LED

F. System status LED

G. ID LED

H. ID button

I. Reset button

J. USB Connector

K. NMI button

L. Video Connector

Implication   The labels for Figure 4 on page 11 will not match the actual functions of the Controls and Indicators on the SR2300 Chassis Front Panel.

Status   The next revision of the SR2300 Chassis Subassembly Product Guide will have these labels corrected.

9. The SR2300 Chassis Technical Product Specification Revision 1.0 and the SR1300 Chassis Technical Product Specification Revision 1.0 incorrectly list the Power Supply input voltage ranges

Problem   The SR2300 Chassis Technical Product Specification Revision 1.0 and the SR1300 Chassis Technical Product Specification Revision 1.0 incorrectly list the Power Supply input voltage ranges. The SR2300 Chassis Technical Product Specification Revision 1.0 currently lists the Power Supply input voltage ranges for the 500Watt Power Supply as 100-120VAC and 200-240VAC on page 6, and for the 480Watt Power Supply as 100-120VAC and 200-240VAC on page 7. The actual input voltage ranges for both of these Power Supplies should be 90-132VAC and 180-264VAC. The SR1300 Chassis Technical Product Specification Revision 1.0 currently lists the Power Supply input voltage ranges for the 350Watt Power Supply as 100-127VAC and 200-240VAC on pages 5 and 12. The actual input voltage ranges for this Power Supply should be 90-132VAC and 180-264VAC.

Implication   The SR2300 Chassis 500Watt and 480Watt Power Supplies will allow the following input voltage ranges: 90-132VAC and 180-264VAC. The SR1300 Chassis 350Watt Power Supply will allow the following input voltage ranges: 90-132VAC and 180-264VAC.

Status   The next revisions of the SR2300 Chassis Technical Product Specification and SR1300 Chassis Technical Product Specification will have the correct Power Supply input voltage ranges added to them.

10. The SR1300 Quick Start User's Guide does not specify that the foam heatsink gasket that is shipped with the 1U Boxed Intel® Xeon™ processors with 512K cache should not be used with the SR1300 chassis

Problem   The SR1300 Quick Start User's Guide does not specify that the foam heatsink gasket that is shipped with the 1U Boxed Intel® Xeon™ processors with 512K cache should not be used with the SR1300 chassis. The 1U Boxed Intel® Xeon™ processors with 512K cache are shipped with a foam heatsink gasket (part number A88102-001). This foam heatsink gasket is intended to be installed on top of the 1U heatsink to block air flow and force the air through the heatsink fins. The SR1300 chassis is shipped with it's own processor air baffle. If the foam heatsink gasket is installed with the SR1300 chassis, the processor air baffle that comes with the SR1300 chassis will not fit down all of the way and may cause the SR1300 chassis lid to be bowed slightly.

Implication   Users should not use the foam heatsink gasket that is shipped with the 1U Boxed Intel® Xeon™ processors with 512K cache with the SR1300 chassis. If the foam heatsink gasket is used with the SR1300 chassis, the chassis lid may be bowed and airflow across the processor heatsinks may not be sufficient.

Status   The next revisions of the SR1300 Quick Start User Guide will have a note added to inform users to not use the foam heatsink gasket that is shipped with the 1U Boxed Intel® Xeon™ processors with 512K cache with the SR1300 chassis.